

SEARCH REQUEST FORM

Scientific and Technical Information Center

(57)

Requester's Full Name: Alex Yufa Examiner #: _____ Date: 10/8/03
 Art Unit: 2133 Phone Number 30 305-0715 Serial Number: 09/888,708
 Mail Box and Bldg/Room Location: 4D21 Results Format Preferred (circle): PAPER DISK E-MAIL

If more than one search is submitted, please prioritize searches in order of need.

 Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: Method and apparatus for bit error rate

Inventors (please provide full names): Perrott, Michael H.

Earliest Priority Filing Date: 6/25/ - 2001

For Sequence Searches Only Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.

The key words - see attached sheets

The invention is regarding integrated circuit (IC) providing a bit error rate evaluation of the stream data.

Simplifying:

- 1) Method for bit error rate estimation
- 2) Apparatus
- 3) Method of chip (IC) manufacturing
- 4) A chip (IC) for bit error rate evaluation

STAFF USE ONLY

Type of Search	Vendors and cost where applicable
NA Sequence (#)	STN _____
AA Sequence (#)	Dialog _____
Structure (#)	Questel/Orbit _____
Bibliographic	Dr.Link _____
Litigation	Lexis/Nexis _____
Fulltext	Sequence Systems _____
Patent Family	WWW/Internet _____
Other	Other (specify) _____